

RSK2H8S2456 STARTER KIT SCHEMATICS

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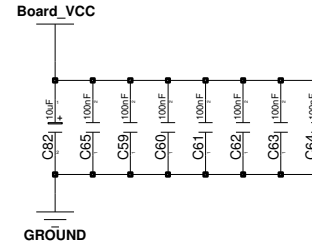
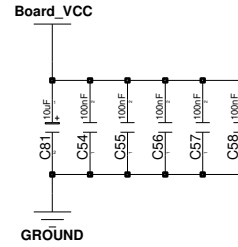
Note :

- R** : Resistor
- RV** : Potentiometer
- L** : Inductor
- U** : Integrated Circuit
- X** : Crystal
- RES** : Reset Switch
- SW** : Switch
- LED** : Light Emitting Diode
- PWR** : Power Jack
- J** : Connector

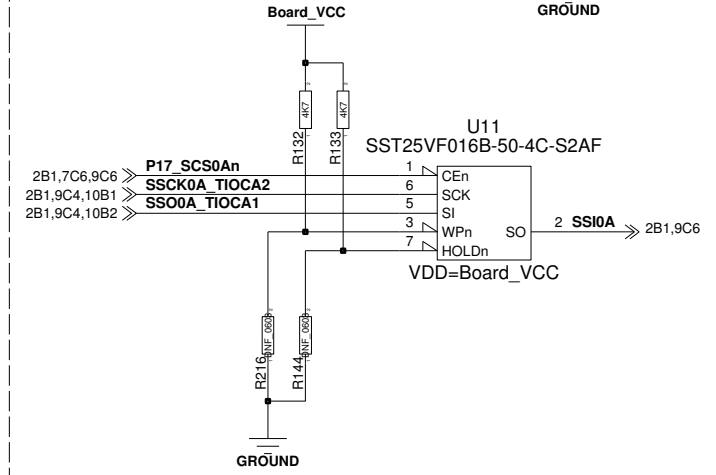
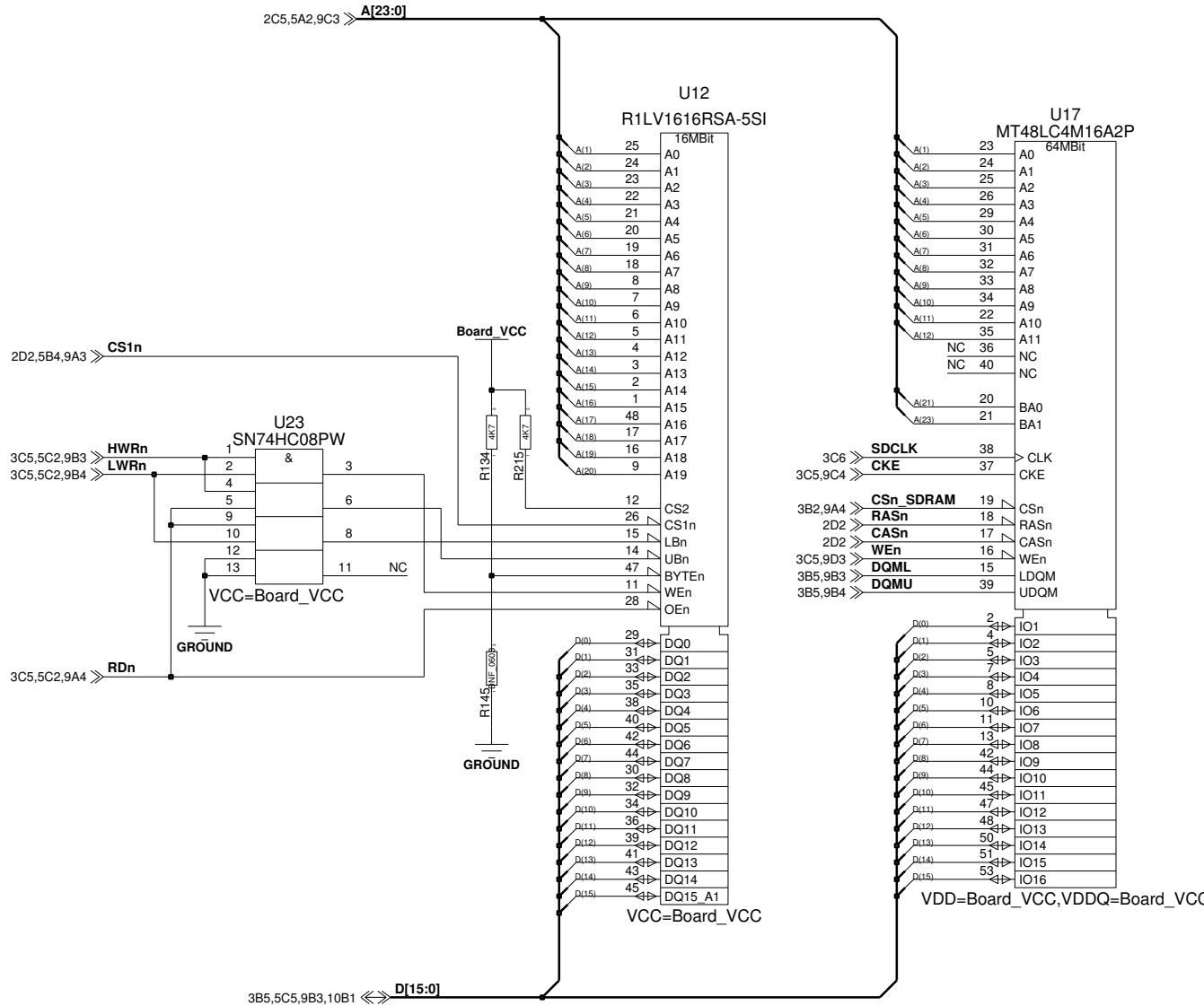
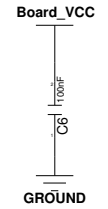
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SCHEMATIC TITLE				
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Index				
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**SRAM (16Mbit)
SDRAM (64Mbit)**



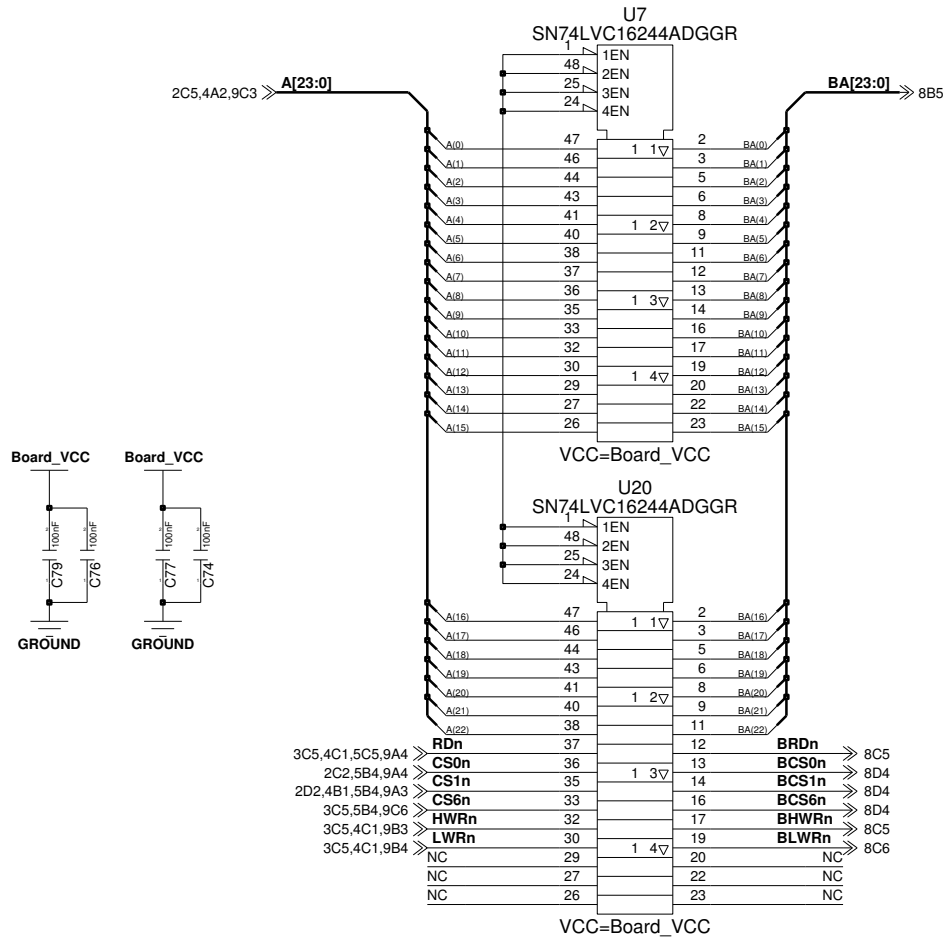
16 Mbit SPI Serial Flash



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Memory				
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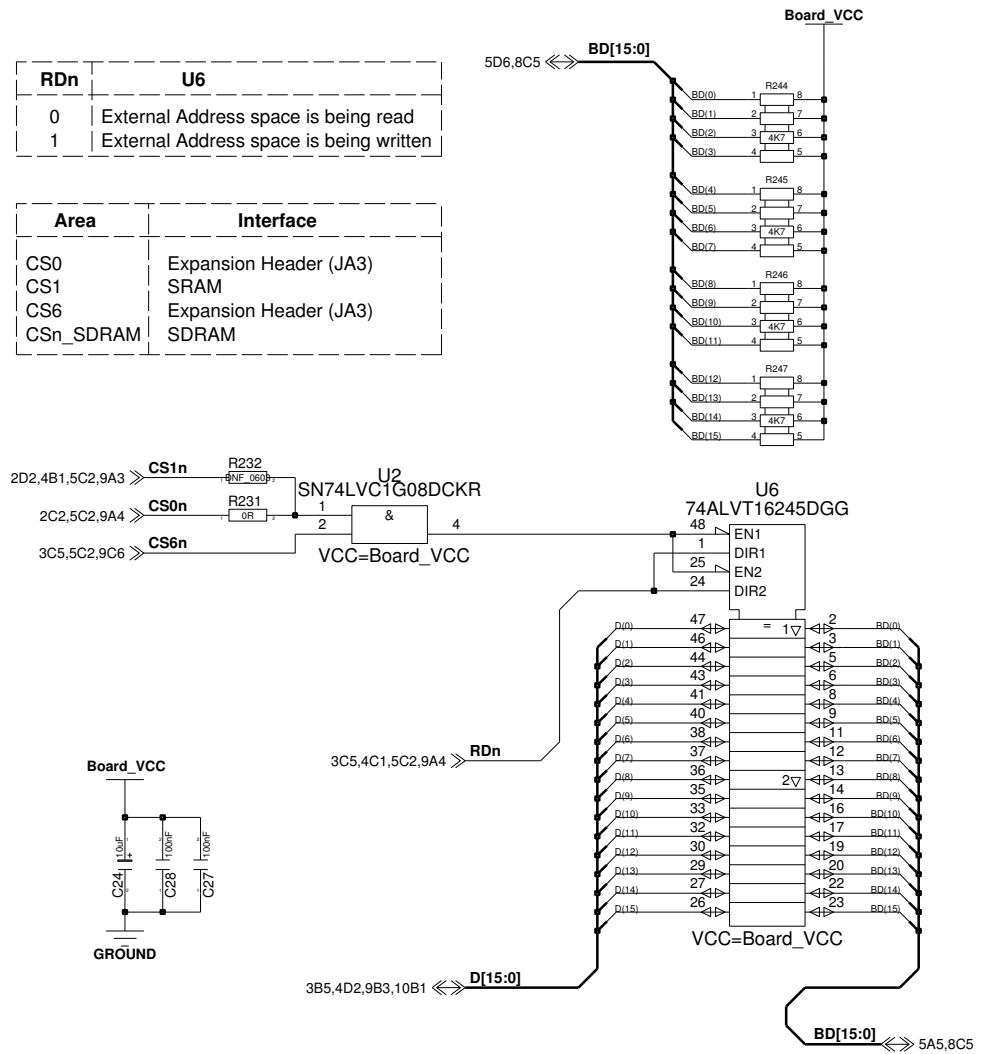
Expansion Address Bus and Control Signal Buffer



Data Bus Direction selector and Buffer

RDn	U6
0	External Address space is being read
1	External Address space is being written

Area	Interface
CS0	Expansion Header (JA3)
CS1	SRAM
CS6	Expansion Header (JA3)
CSn_SDRAM	SDRAM



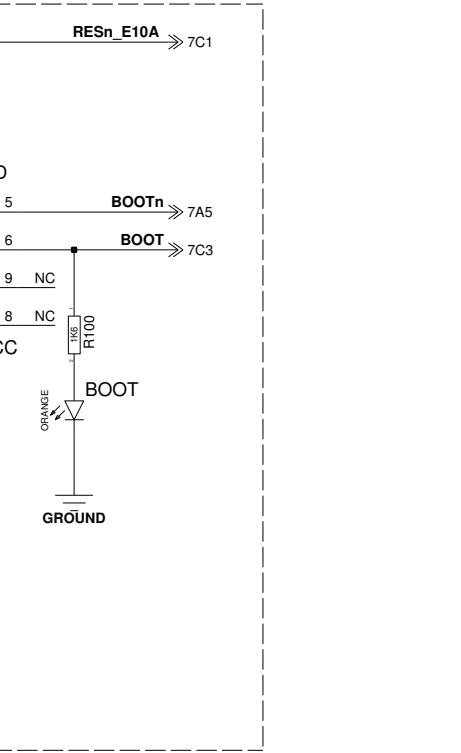
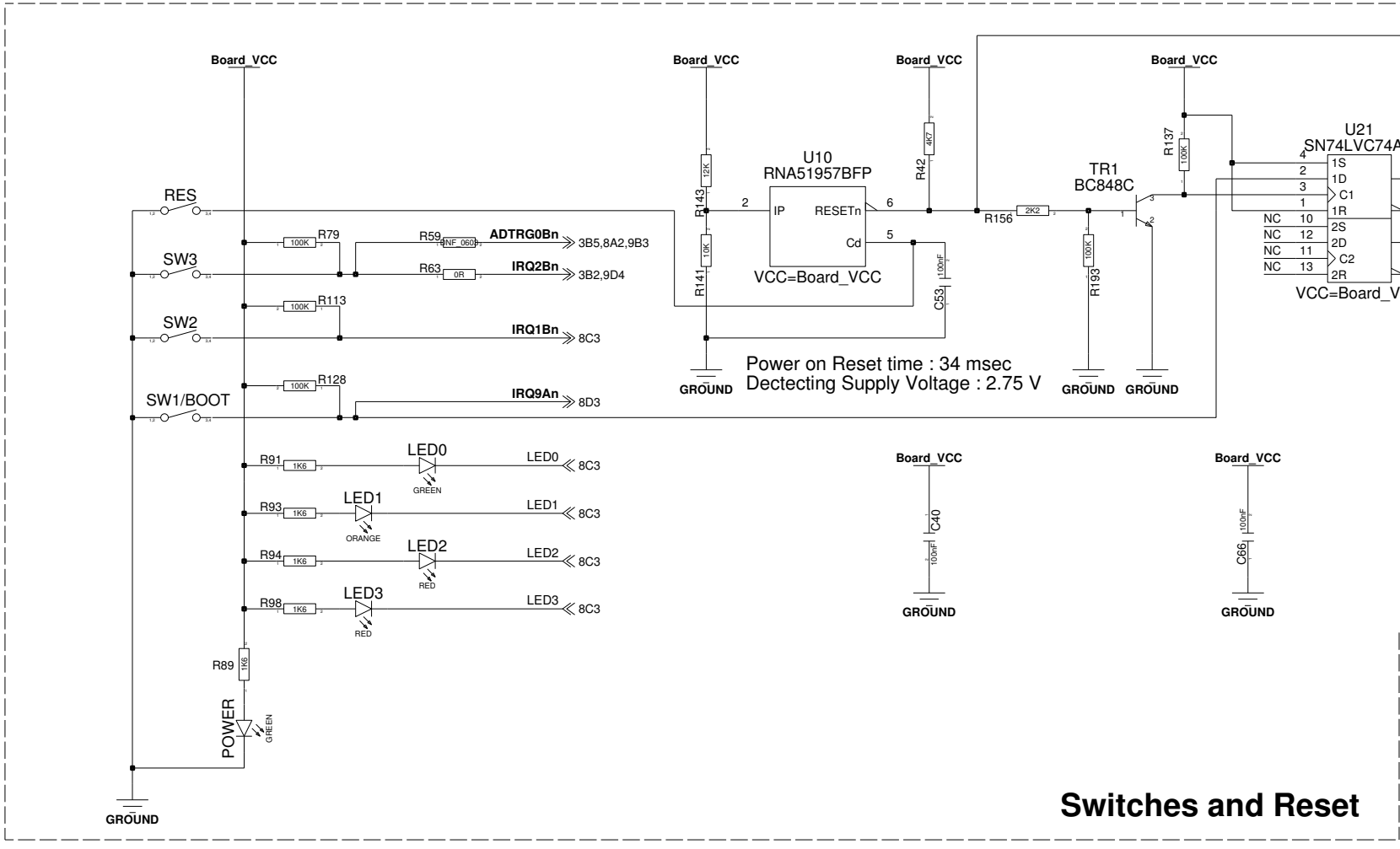
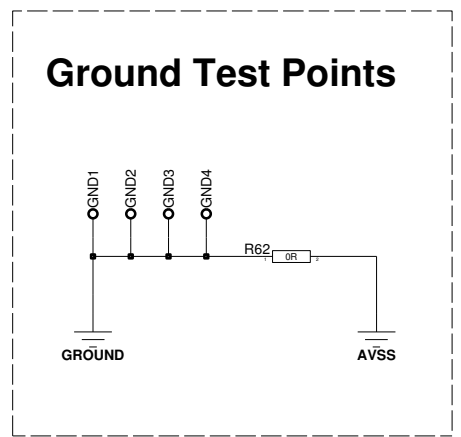
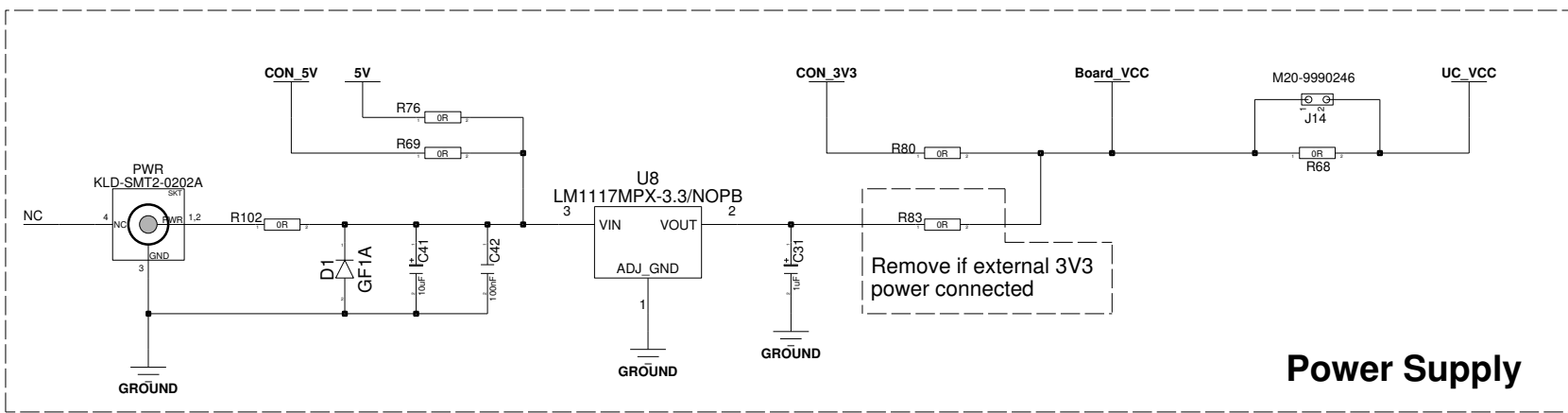
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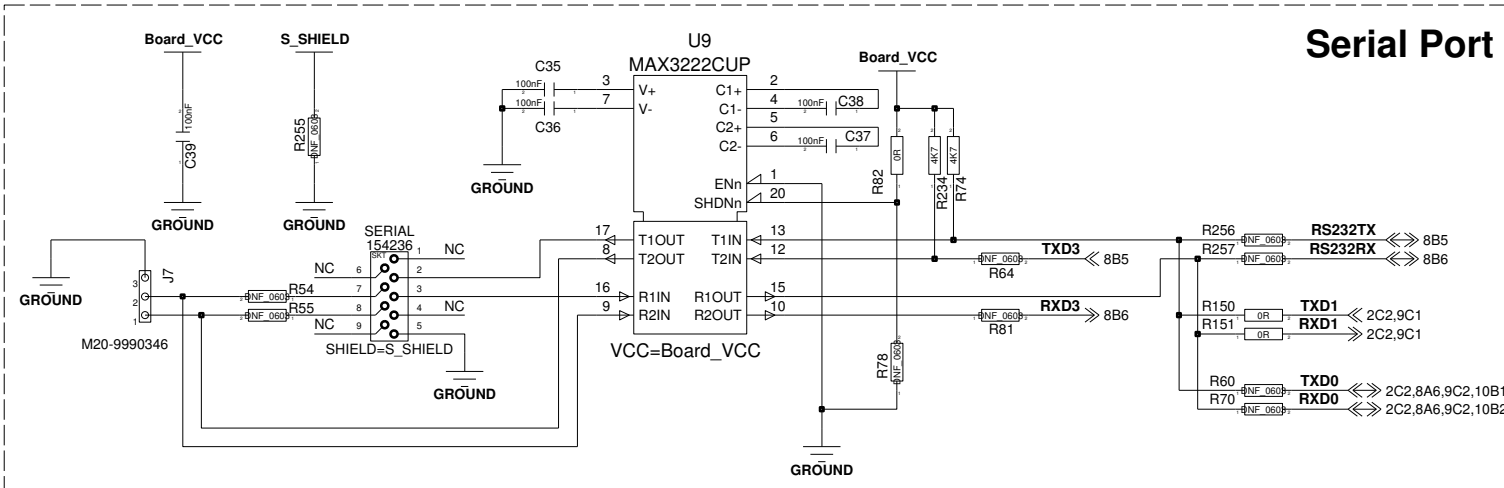
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Address and Data Bus Buffer

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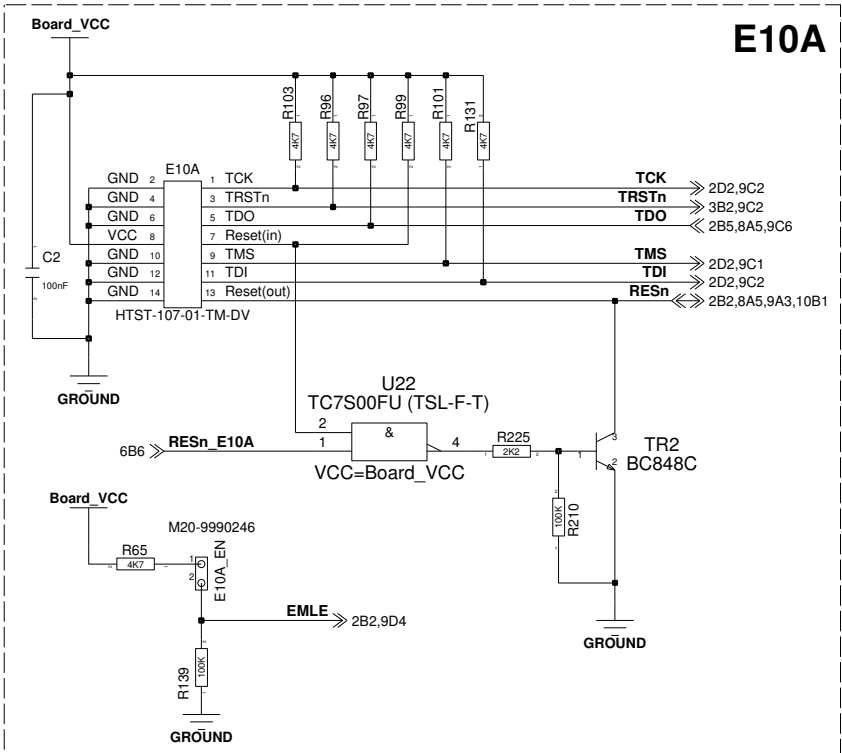
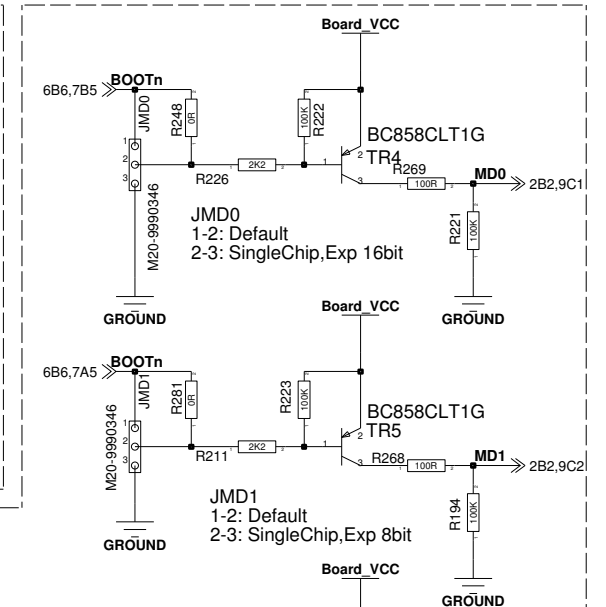


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Switches, Power Supply and Reset				
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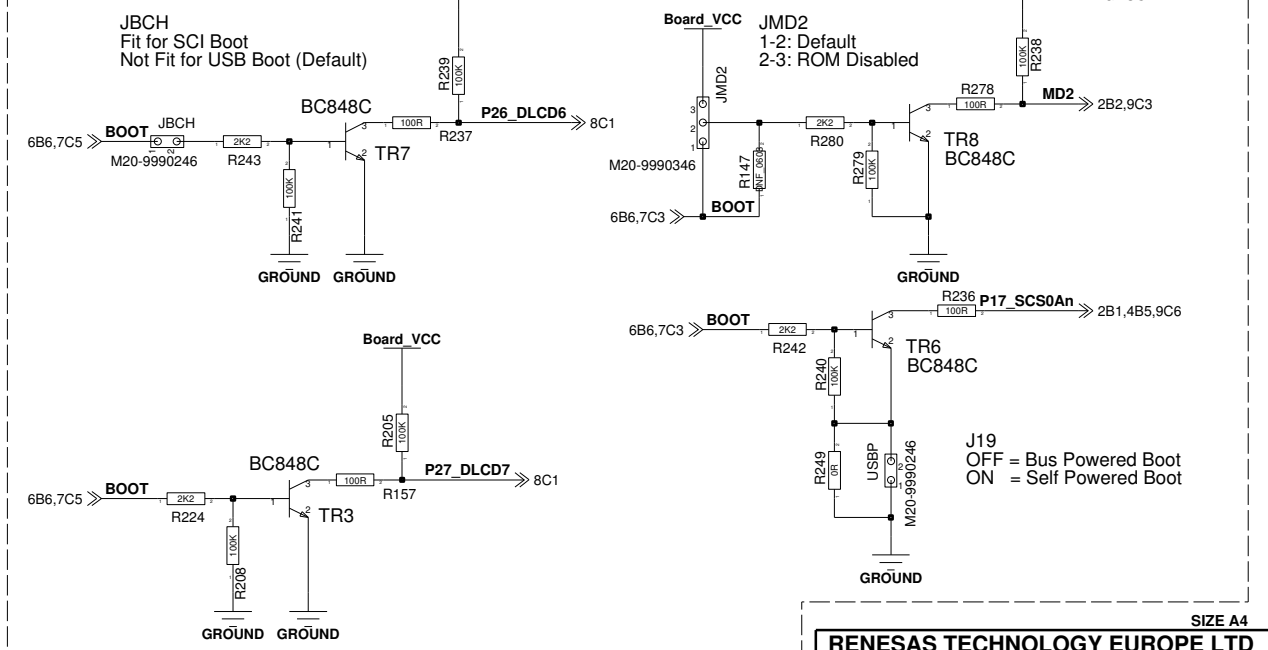


Serial Port



E10A

CPU Mode Selection



Mode Type	MD2	MD1	MD0	P27	P26	Clock Frequency	On Chip ROM	External Data Bus Initial Value	External Data Bus Max Value
Expansion Mode	0	0	1	X	X	---	Disabled	16 bits	16 bits
Expansion Mode	0	1	0	X	X	---	Disabled	8 bits	16 bits
SCI Boot Mode	0	1	1	0	0	---	Enabled	---	16 bits
USB Boot Mode	0	1	1	0	1	16 MHz	Enabled	---	16 bits
Expansion Mode **	1	0	0	X	X	---	Enabled	8 bits	16 bits
Single Chip Mode	1	1	1	X	X	---	Enabled	---	16 bits

** Default Mode

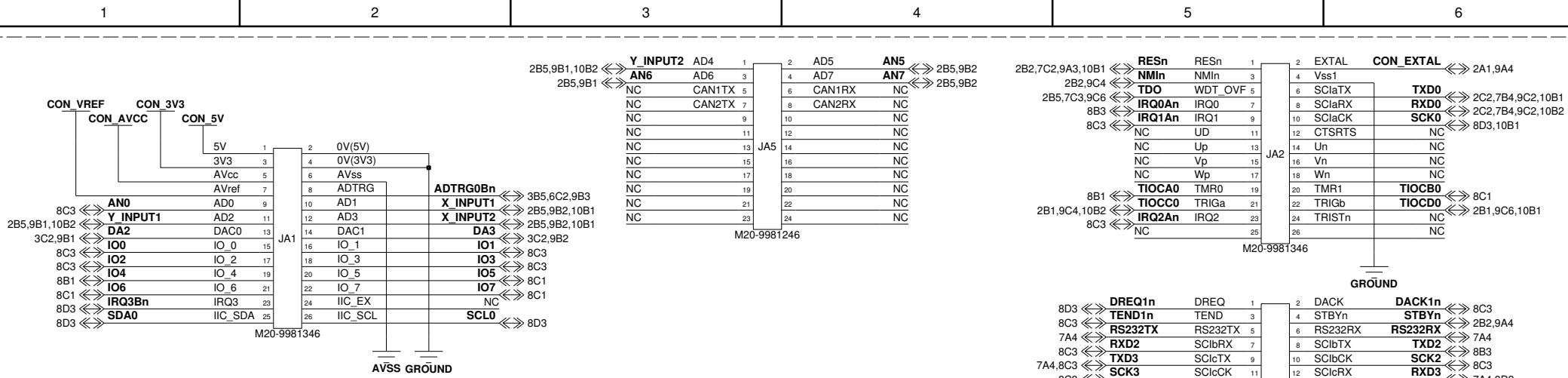
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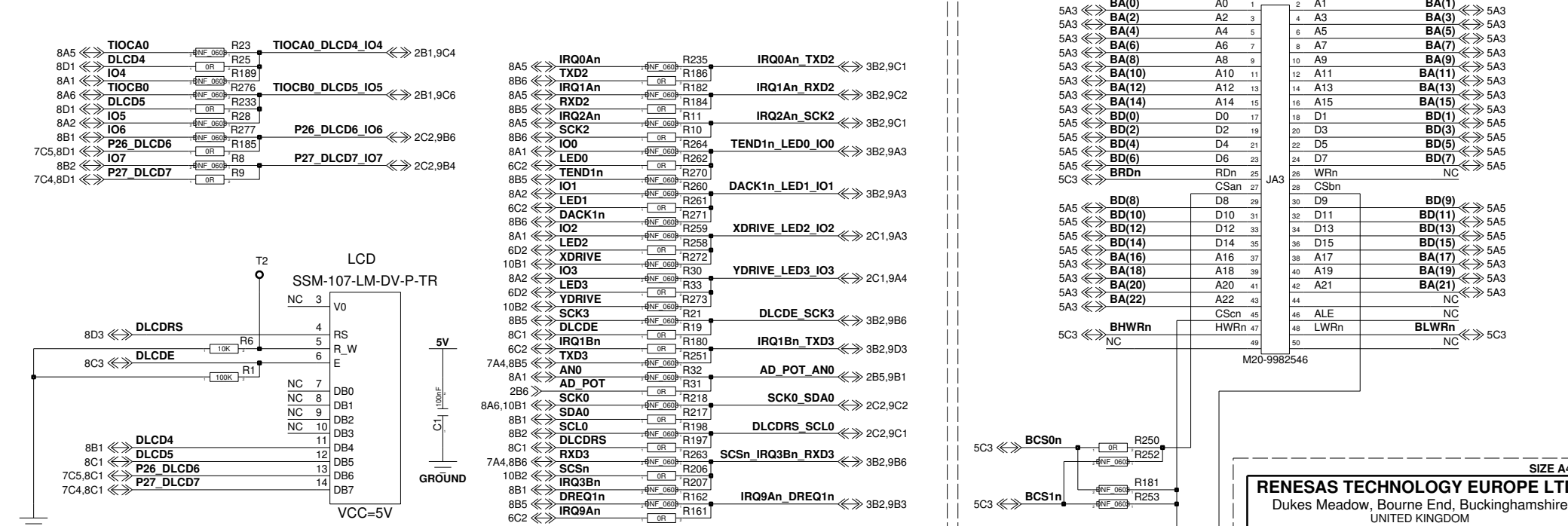
SHEET TITLE
E10A Debug Connector and Serial Port

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APPLICATION HEADERS

DEBUG LCD and OPTION LINKS



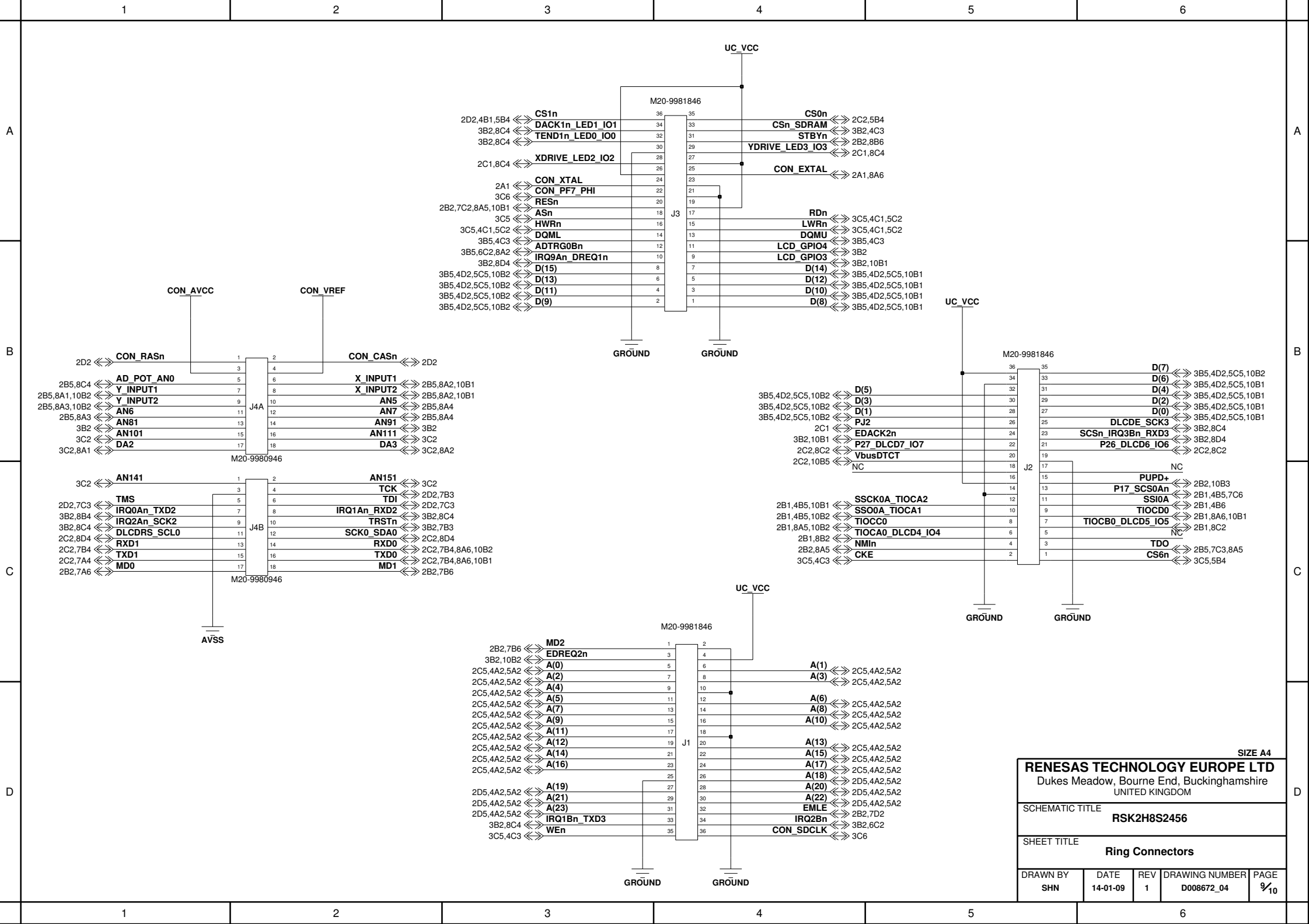
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Application Headers

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Ring Connectors				
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